

TURBO DECODER ARCHITECTURE WITH MINI-TRELLIS SISO
ABSTRACT OF THE DISCLOSURE

The Turbo decoding architecture has a first soft-input soft-output (SISO) device having a complex trellis structure and a Turbo decoder loop with a second SISO device having a simpler trellis structure. Coded information from a channel is processed by the first SISO device to generate a soft-output based on the coded information. The soft-output is then processed in an iterative loop using the second SISO device. The second SISO device interacts with a decoder device to produce a value representative of the transmitted information.

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